

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki, et al.

Art Unit : 2823

Patent No. : 7,943,968

Examiner : Matthew Smith

Issue Date : May 17, 2011

Conf. No. : 6059

Serial No. : 08/994,038

Filed : December 18, 1997

Title : CHARGE TRANSFER SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD THEREOF

Attn.: Certificate of Corrections Branch

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL OF REQUEST FOR CERTIFICATE OF CORRECTION

Applicants hereby request that a certificate of correction be issued for the above patent in accordance with the attached request.

All errors sought to be corrected were made in printing by the Patent and Trademark Office, and no fee is believed to be due.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: October 23, 2012

/John F. Hayden/

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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APPLICATION NO.: 08/994,038

ISSUE DATE: : May 17, 2011

INVENTOR(S) : Satoshi Teramoto, Shunpei Yamazaki

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, line 64, in claim 11 replace "su" with --so--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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